

Notice of Allowability

Application No.

10/724,657

Examiner

Pho M. Luu

Applicant(s)

CHOU, MIN-CHUNG

Art Unit

2824

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to Amendment filed on 04/28/06.
2. ☒ The allowed claim(s) is/are 1-13 and 17-24.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying Indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☒ Other Search History.



RICHARD ELMS
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800

Allowance

1. Acknowledgment is made of applicant's Amendment, filed 28 April 2006. The changes and remarks disclosed therein were considered.
2. Claims 14-16 have been canceled.
3. Claims 1-13 and 17-24 are pending in the application.
4. Claims 1-13 and 17-24 are allowed.

The following is an examiner's statement of reasons for allowance:

There is no teaching or suggestion in the prior art to provide:

“during the burn in test, the leak current limited unit limits the current in each word line to a predetermined word line current value and a word line stress voltage is provided via the leak current limited unit to stress each column of memory cells connected to one word line” in a memory device for burn in test as claimed in the independent claim 1. Claims 2-9 are also allowed because of their dependency on claim 1; or

“applying a predetermined high voltage to the source of the first PMOS transistor so that both of the source of the first PMOS transistor and the second PMOS transistor are kept at the predetermined high voltage and providing a predetermined low voltage to the output of the word line driver via a word line” in a method for keeping the output of a word line driver in high impedance state during a burn in test on a memory cell as claimed in the independent claim 10. Claim 11 is also allowed because of their dependency on claim 10; or

“a switch connected to the data in unit for switching between the normal data in path and the burn in test path, the switch sending the input data selected from either the normal data in path or the burn in test path to the data unit” in a switch circuit as claimed in the independent claim 12. Claim 13 is also allowed because of their dependency claim 12; or

“a plurality of switch circuits wherein each switch circuit is connected to one bit line and each switch circuit switches between a normal data in path and a burn in test path in the burn in test on a row of memory cells” in a memory device for a burn in test as claimed in the independent claim 17. Claims 18-24 are also allowed because of their dependency claim 17.

5. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled “Comments on Statement of Reasons for Allowance.”

Conclusion

6. Any inquiry concerning this communication or earlier communications from the Examiner should be directed to Pho M. Luu whose telephone number is 571.272.1876. The examiners can normally be reached on M-F 8:00AM – 5:00PM.

If attempts to reach the Examiner by telephone are unsuccessful, the Examiner's Supervisor, Richard Elms, can be reached on 571.272.1869. The official fax number for

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the organization where this application or proceeding is assigned is 703.872.9306 for all official communications.

The information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

PML

14 June 2006